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MAY 22 2006

Appln. No. 09/832,435
Amdt. dated May 22, 2006
In Reply to Office Action of Feb. 22, 2006

detection modules, each of said error detection modules determining a number of errors associated with a corresponding one said plurality of potential phases of said codeword using the syndromes produced by an associated one of said plurality of syndrome computing modules.

23. (Original) The codeword synchronization system of claim 20 wherein said sampling arrangement includes:

a sampling circuit; and
a clock coupled to said sampling circuit, said clock being operative at N times a data rate of said received data stream.

24. (Previously Presented) The codeword synchronization system of claim 23 wherein said sampling arrangement further includes a demultiplexer having an input port connected to said sampling circuit and a set of N output ports connected to said set of N codeword synchronization modules.

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